

GA-F2A85XM-HD3

PAGE TITLE Revision : 1.01

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	APU DDRIII MEMORY
05	APU CONTROL
06	APU UMI, GFX, GPP
07	APU POWER & GND
08	DDRIII CHANNEL A
09	HUDSON UMI, PCIE, PCI, CPU, LPC
10	HUDSON ACPI, GPIO, USB, AUDIO
11	HUDSON SATA,SPI,HWM
12	HUDSON POWER & GND
13	PCIE x16 , x1
14	PCI SLOT ,HDMI
15	ITE 8728 , Dual-BIOS , KB/MS
16	COM ,RGB ,DVI
17	FAN , HWMO , F_USB
18	ALC887-VD2 CODEC
19	ATX, FRONT PANEL
20	POWER SEQUENCE
21	VCORE (ISL62773)
22	VCORE MOS
23	DDR POWER, 5VDUAL , ERP
24	RTL8111F-VL
25	VCC_SB , APU_VDDP , VCC11_DUAL , VDDA25

APU_VDDIO_SUS=DDR15V
APU_VTT_SUS=DDRVTT
APU_VDDP_RUN=APU_VDDR_RUN=APU_VDDP

+1.1V_RUN=FCH_VDD_11_RUN=VCC_SB

+3.3V_RUN=VCC3
+3.3V_ALW=3VDUAL

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Title

COVER SHEET

Size

Custom

Document Number

GA-F2A85XM-HD3

Rev

1.01

Date:

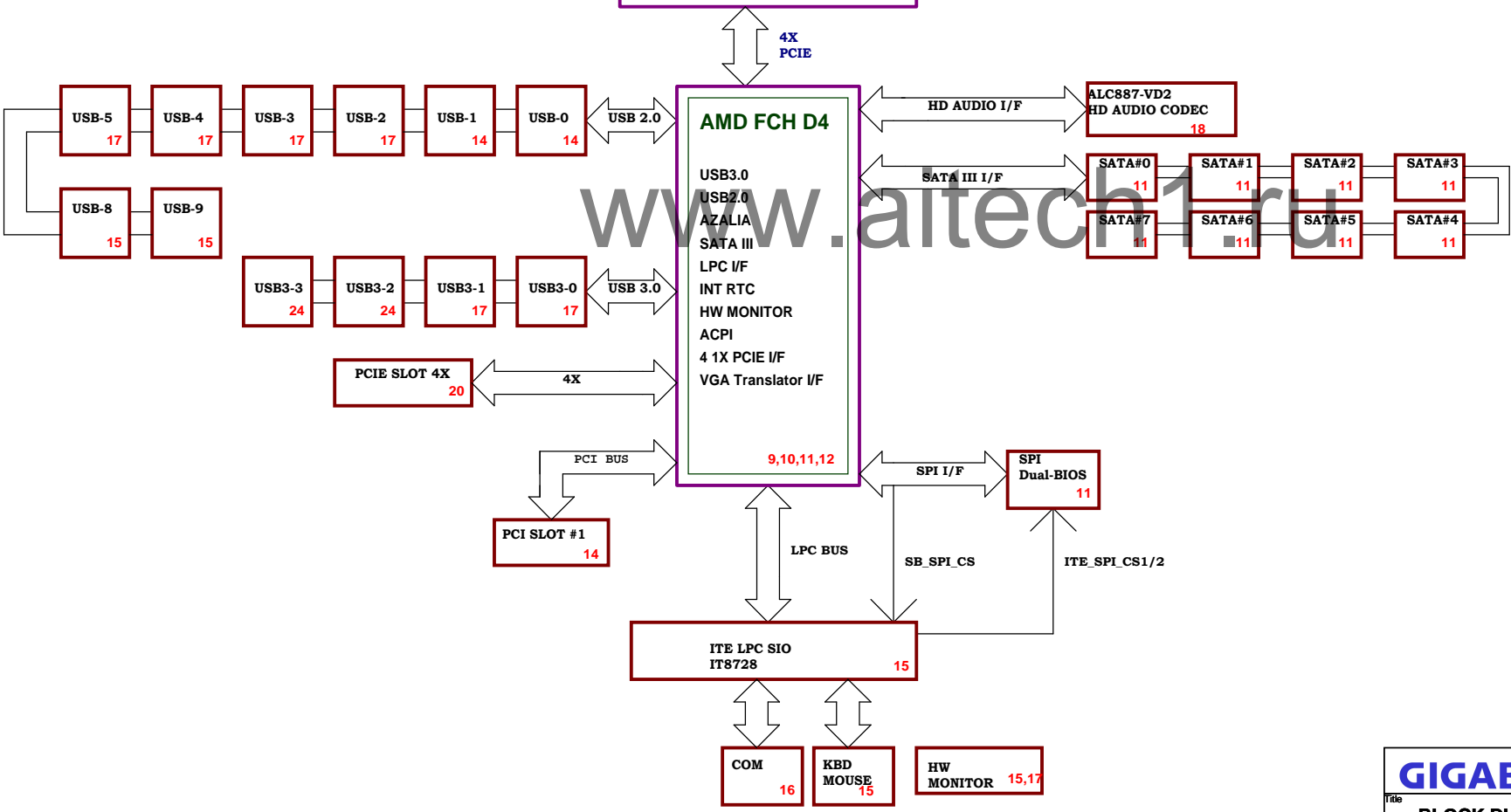
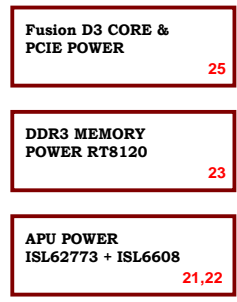
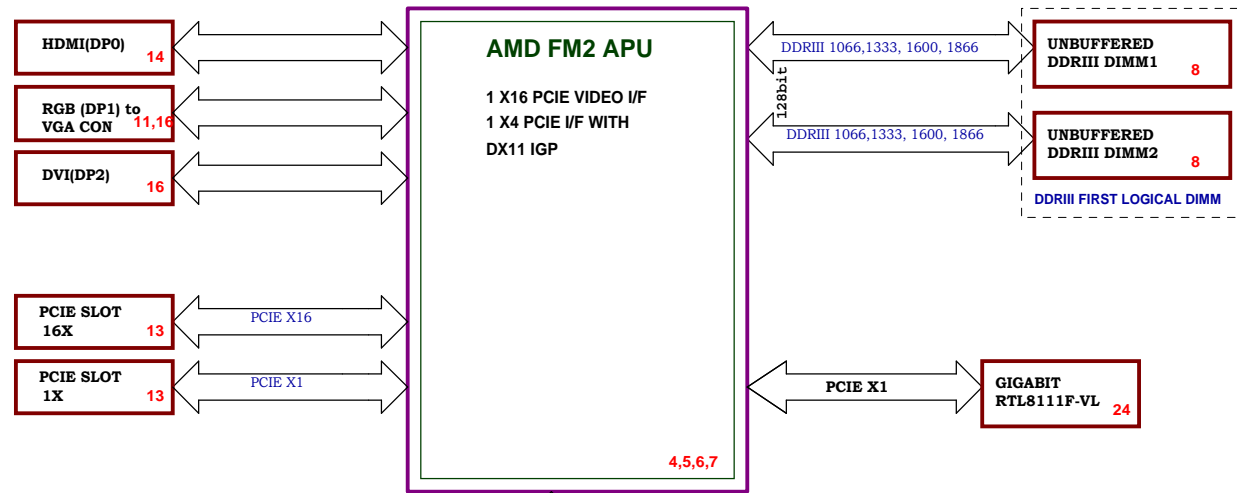
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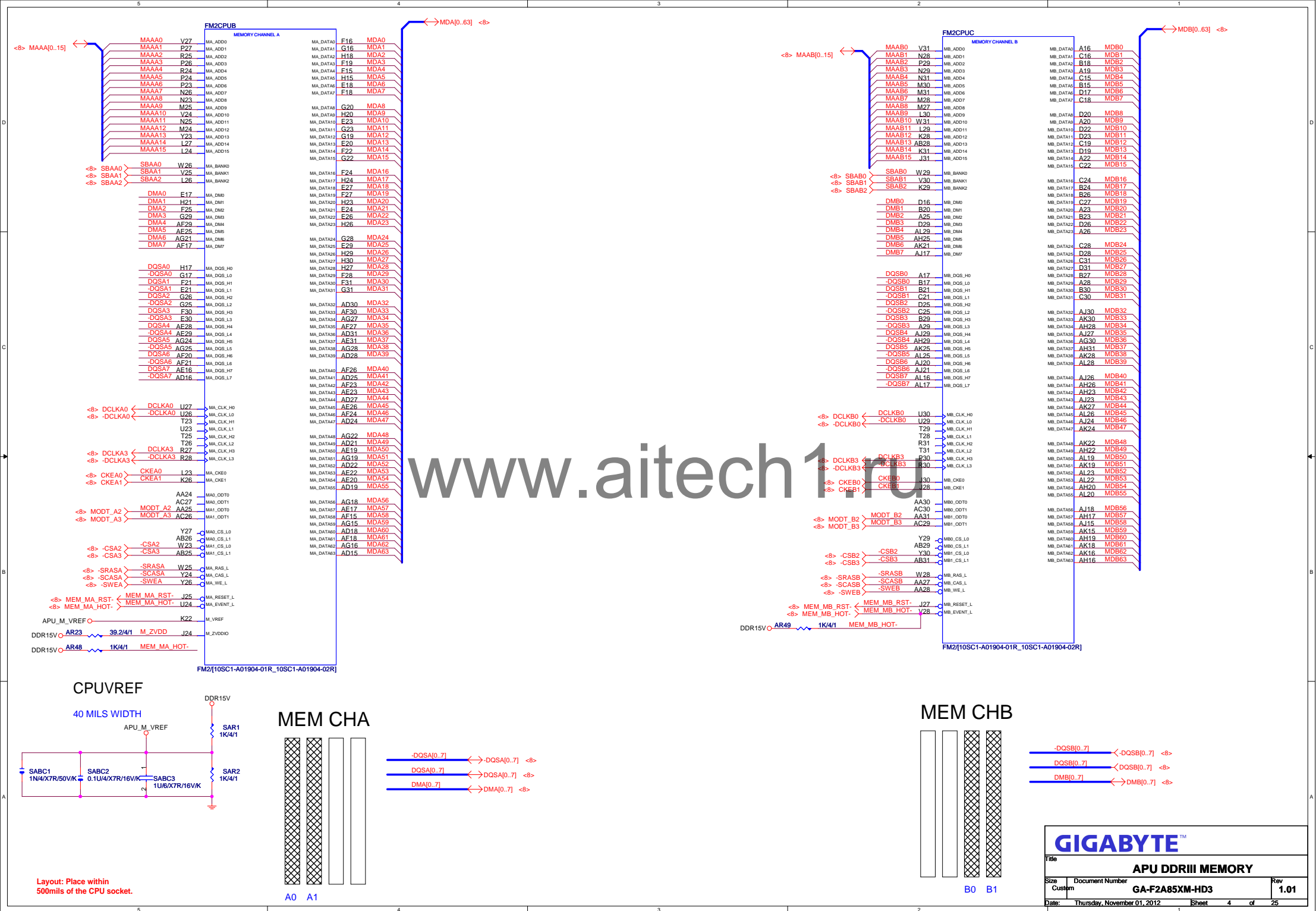
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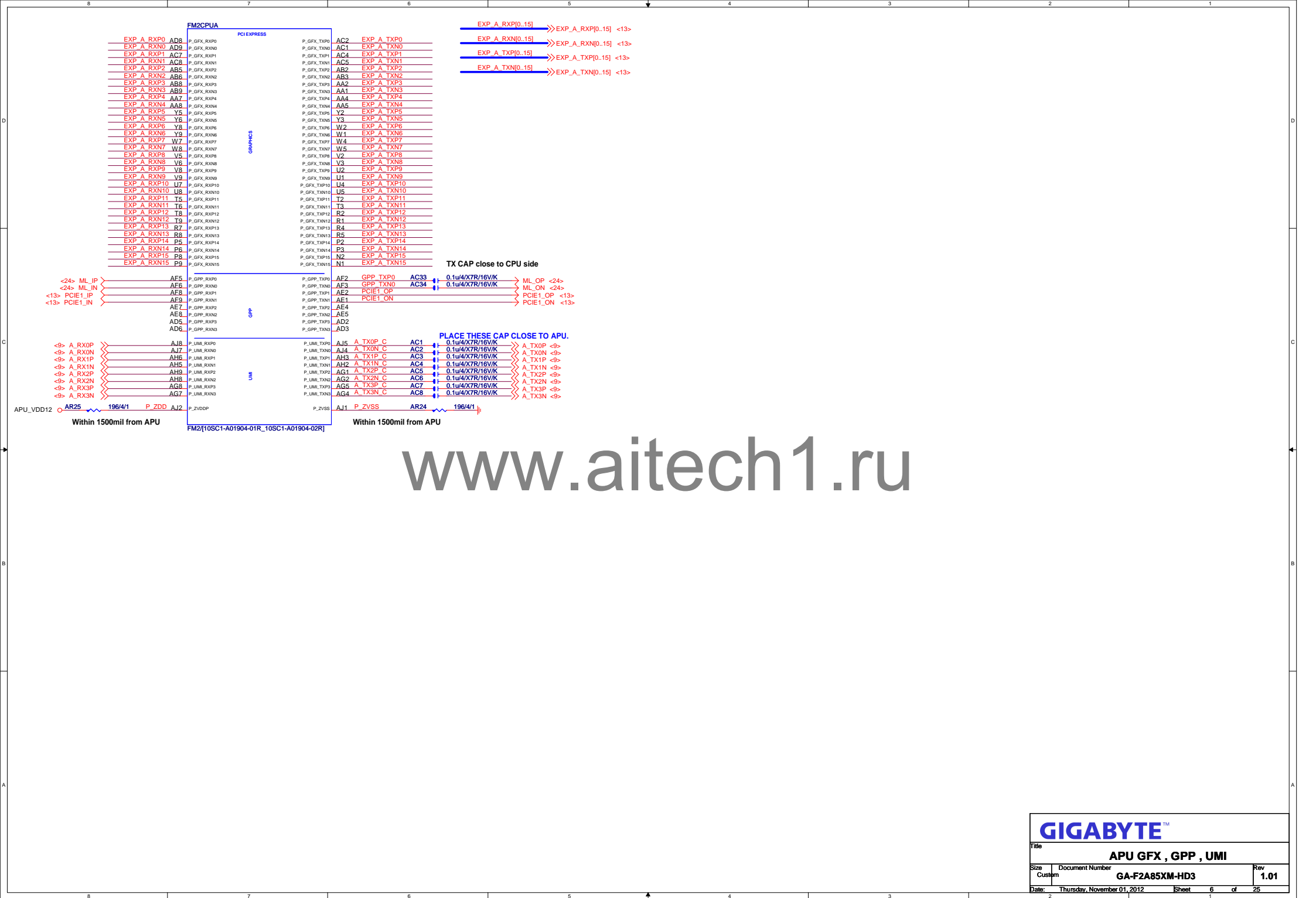
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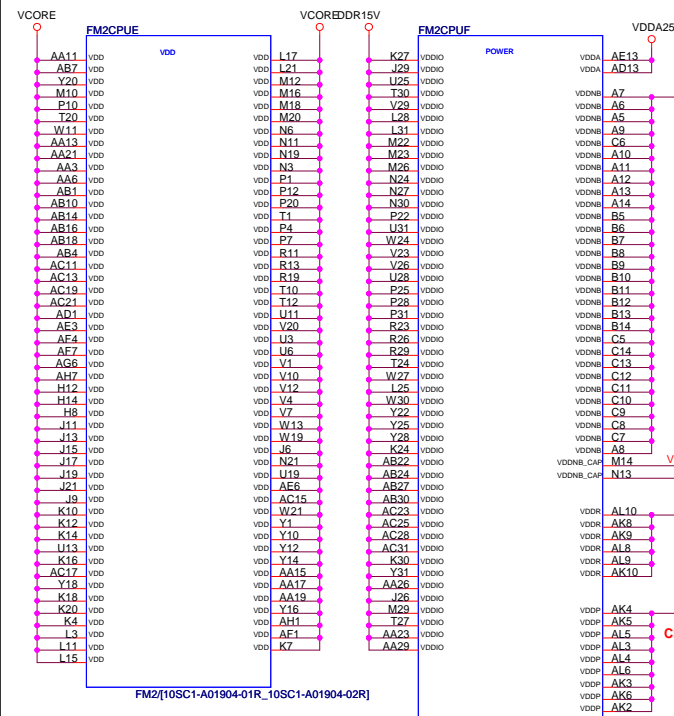
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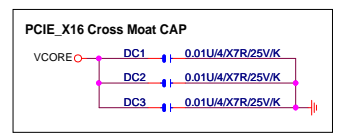
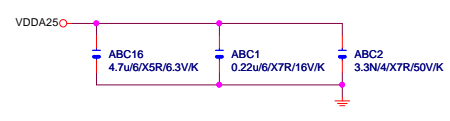


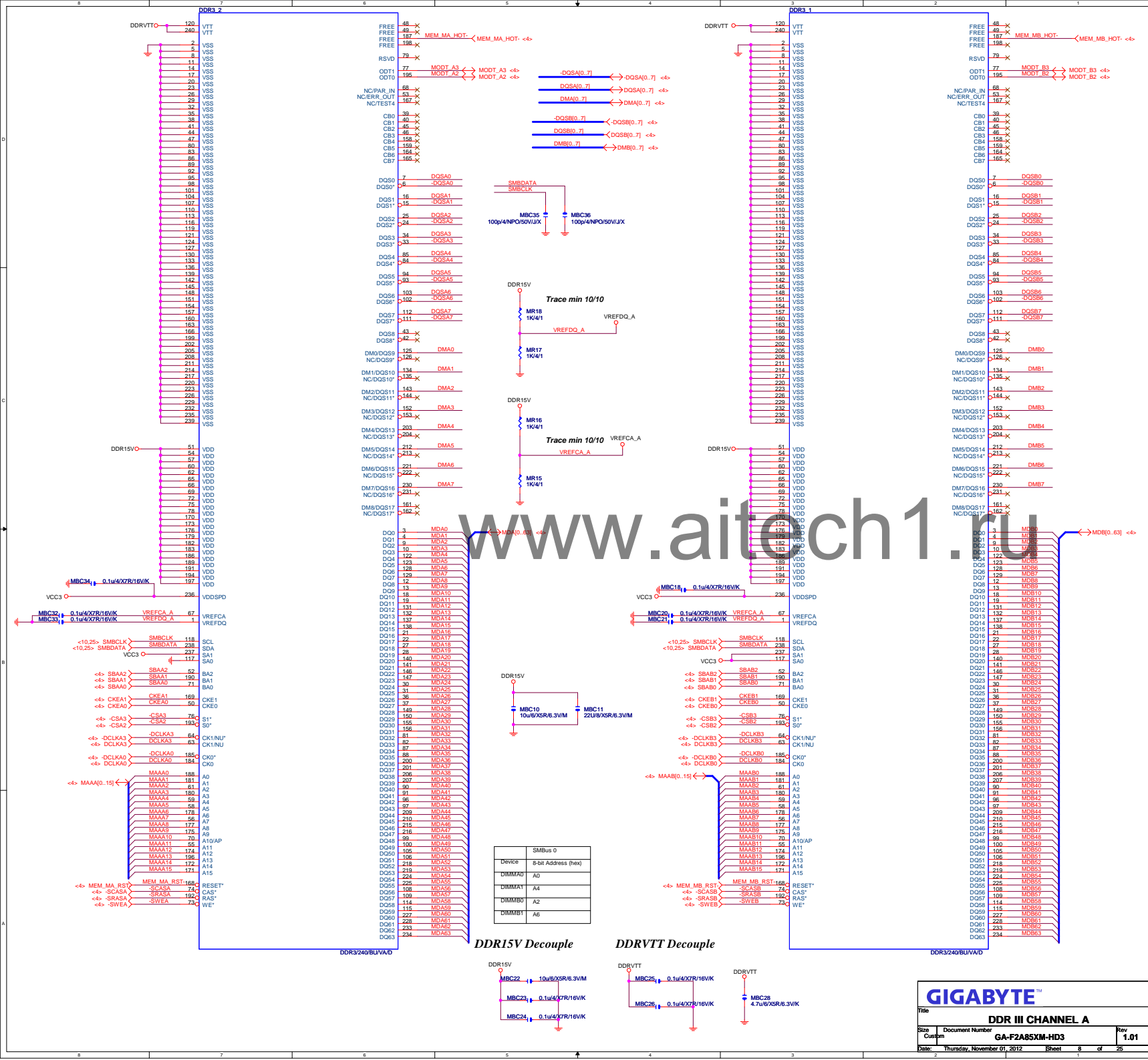


DDR15V=1.25V/1.35V/1.5V(DDR3)



GND:232 pin,
VCORE:99 pin,
VCORE_NB: 30 pin,
DDR15V:49 pin,
VDDP:9 pin, VDDR:9
pin, VDDA25:2 pin,
VDDNB_CAP:2
pin, Total:430 pin.

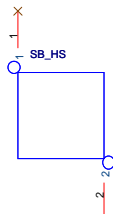




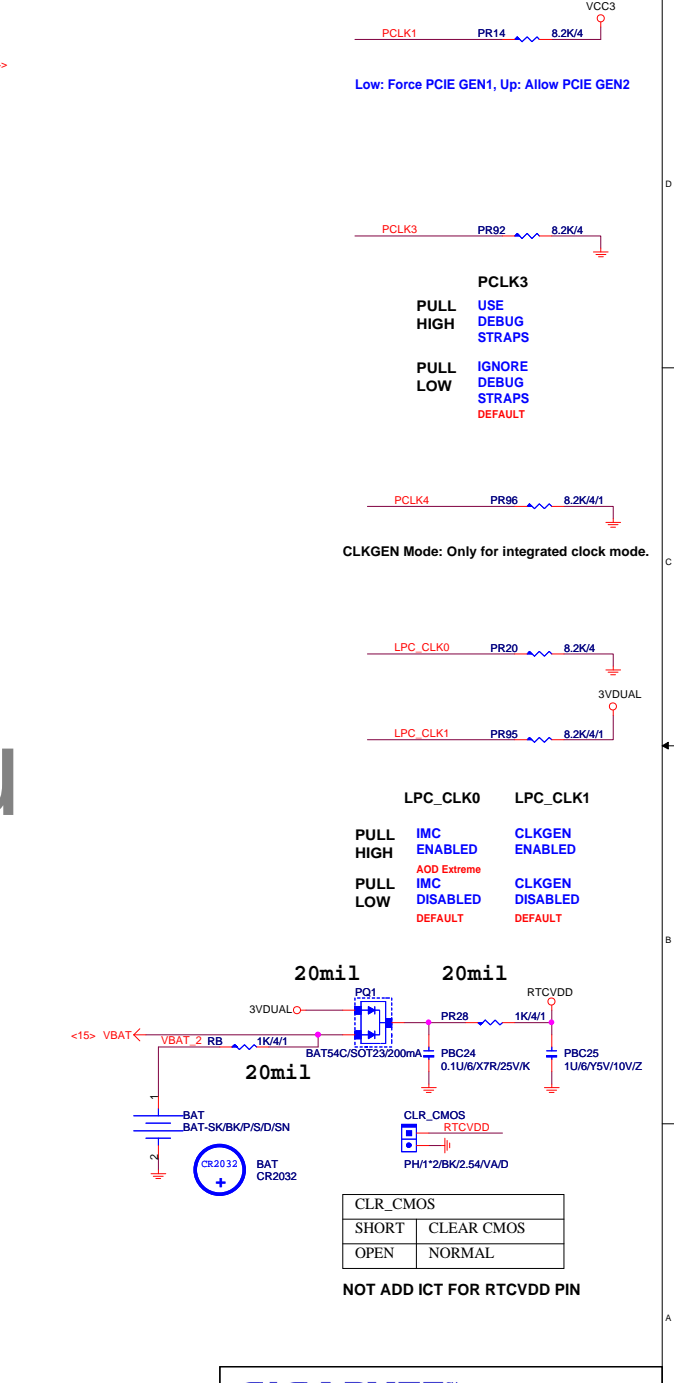
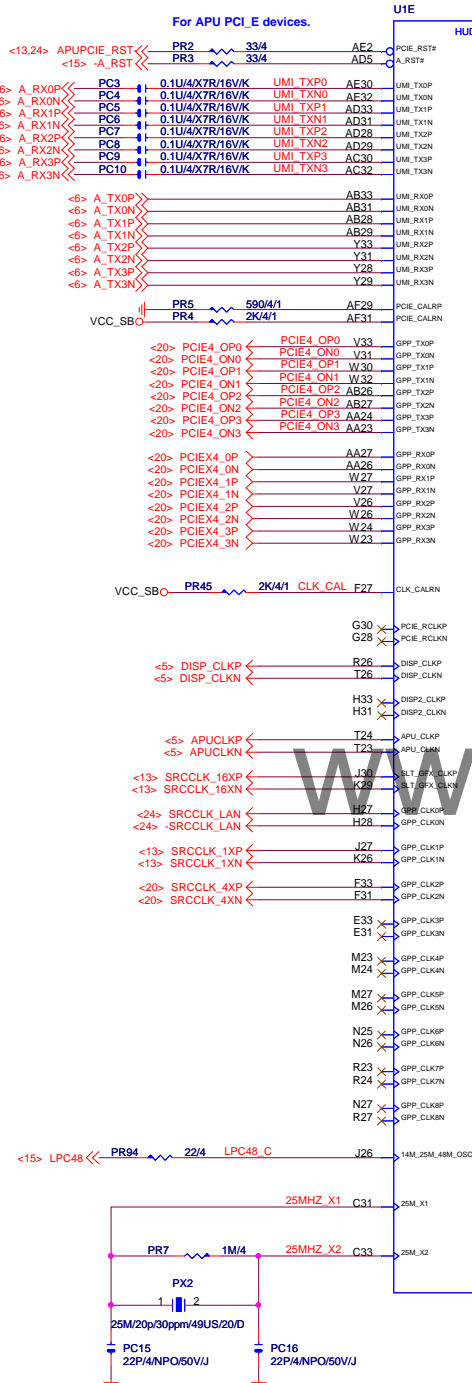
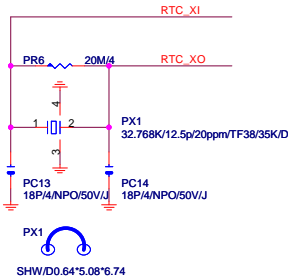


PLACE THESE PCIE AC COUPLING CAPS CLOSE TO SB850

S.B HEATSINK



SB_HS[12SP2-SA0301-01R_12SP2-SA0301-02R_12SP2-SA0301-03R]



GIGABYTE™

Hudson D4 PCIE/PCI/CPU/LPC

Size Custom

Document Number

GA-F2A85XM-HD3

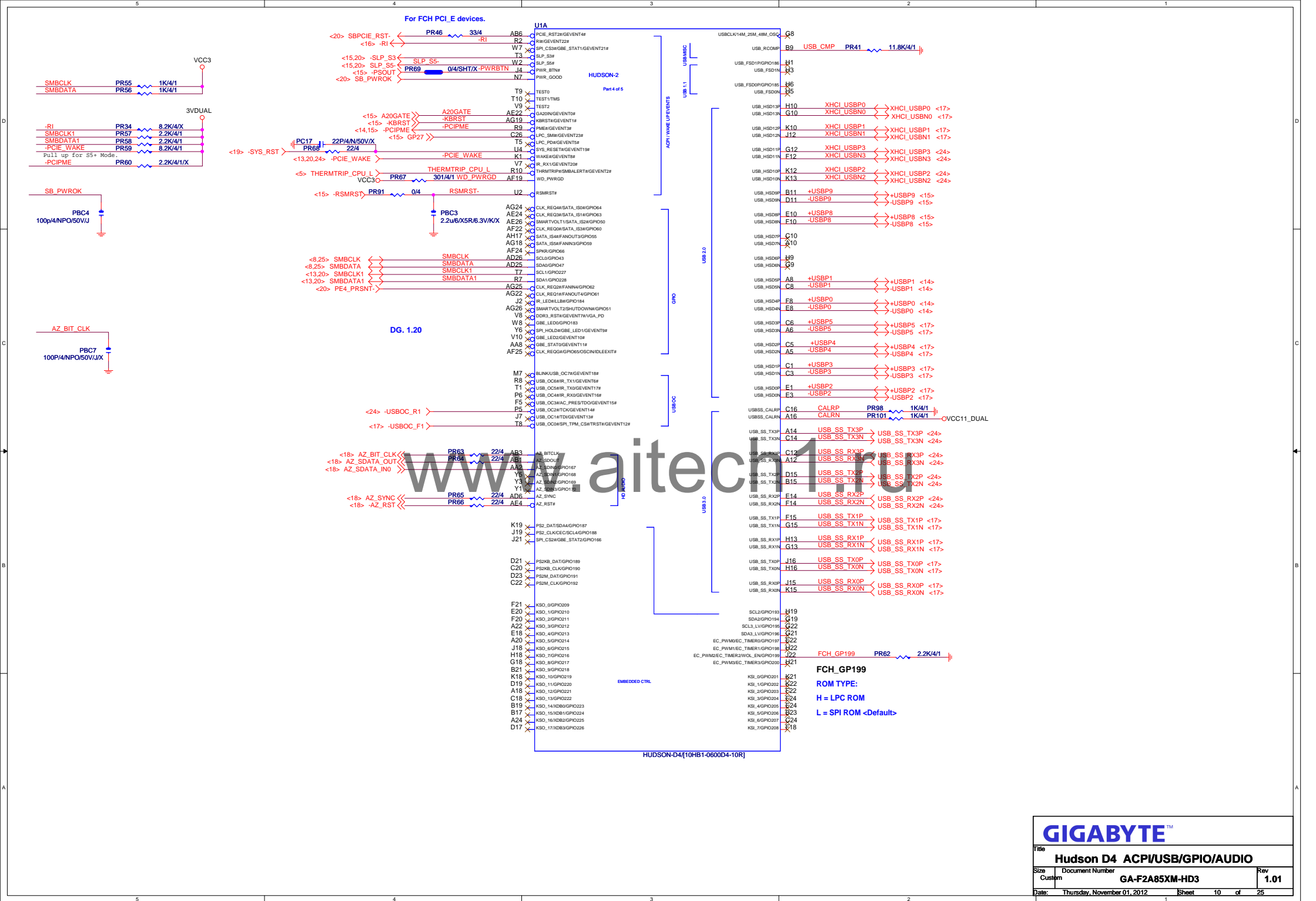
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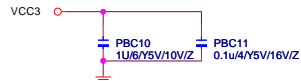
Sheet 9 of 25

Rev 1.01

CLR_CMOS	
SHORT	CLEAR CMOS
OPEN	NORMAL

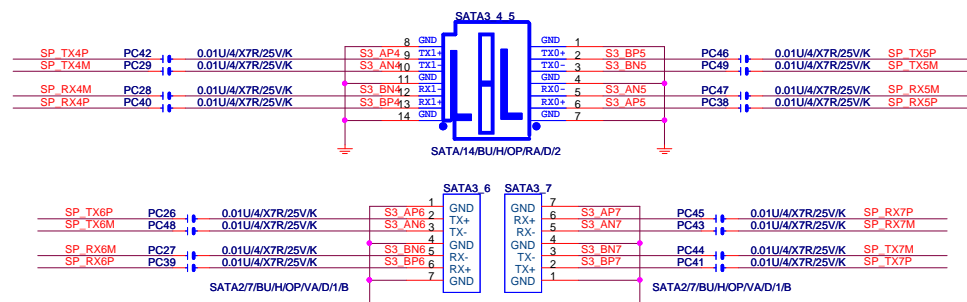
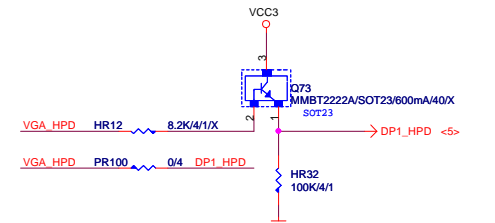
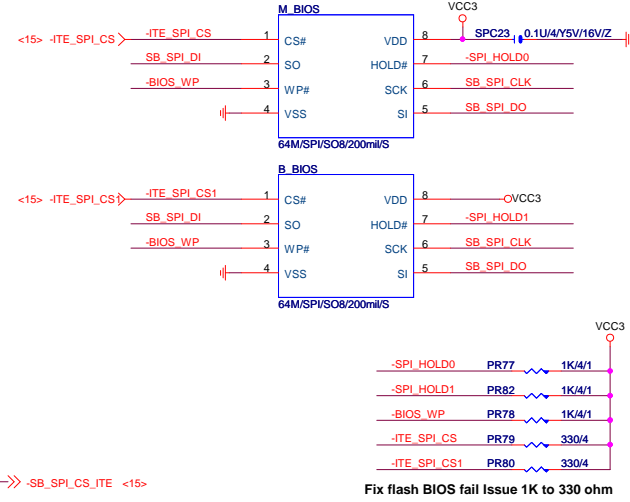
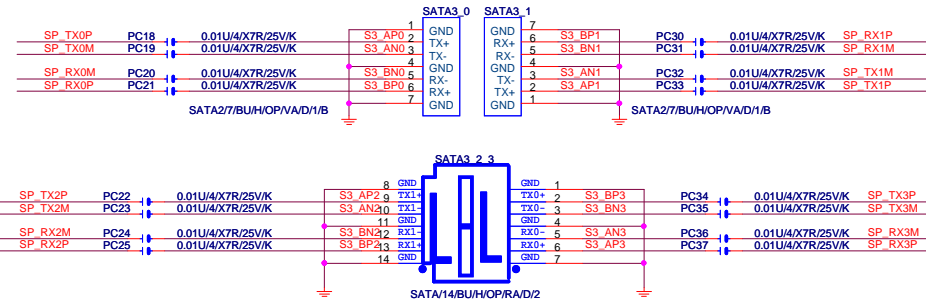
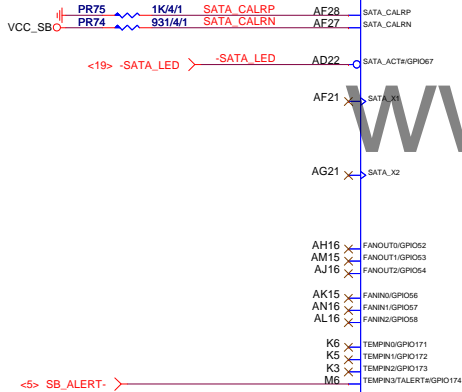
NOT ADD ICT FOR RTCVDD PIN





SATA 6~7 for Hudson D4.

PLACE SATA_CAL RES VERY CLOSE TO BALL OF U1

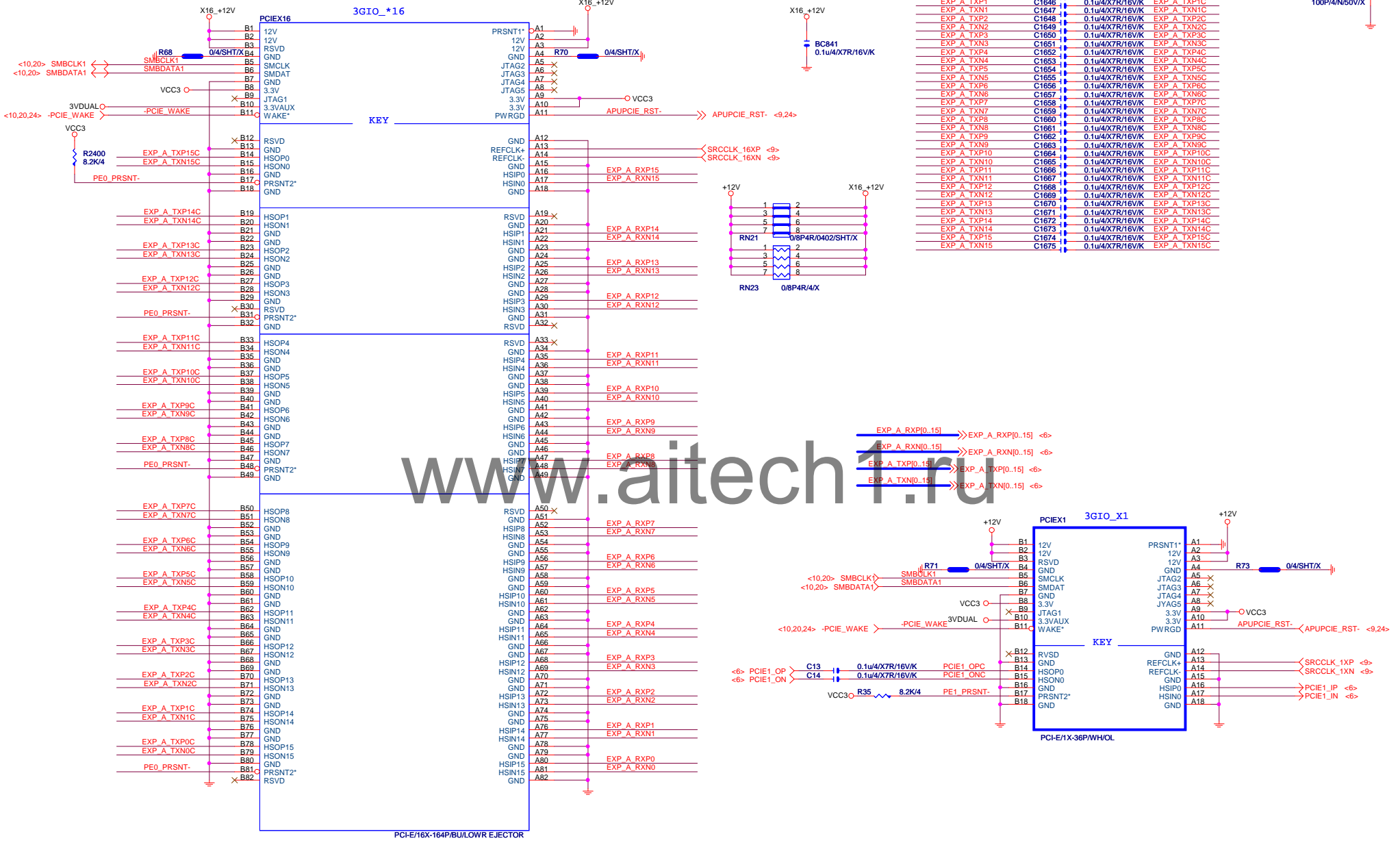


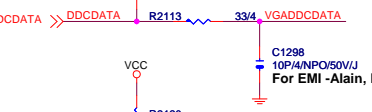
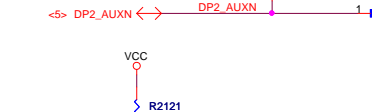
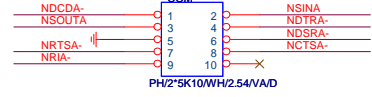
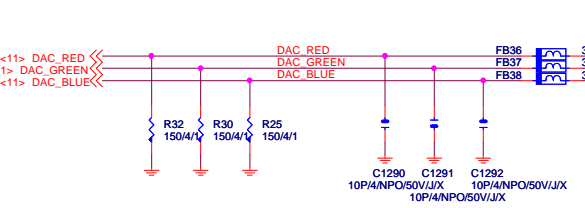
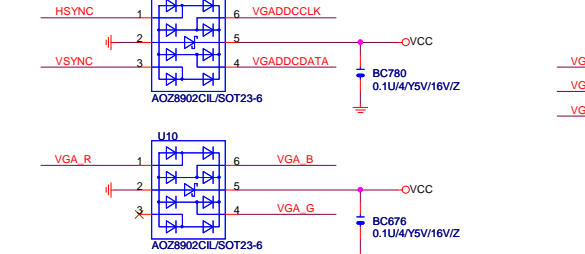
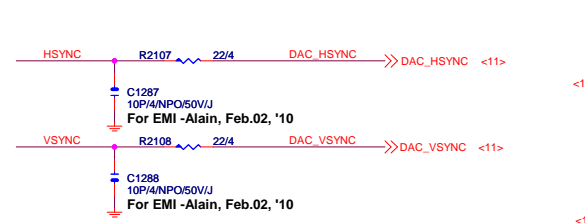
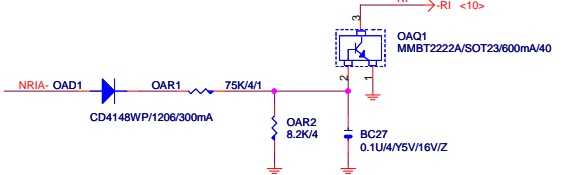
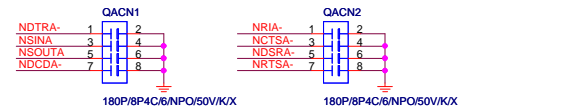
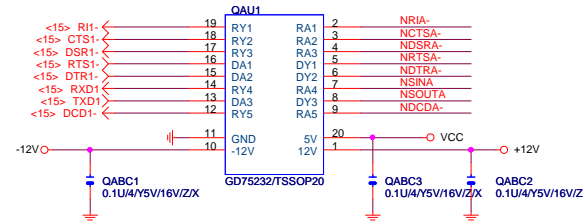
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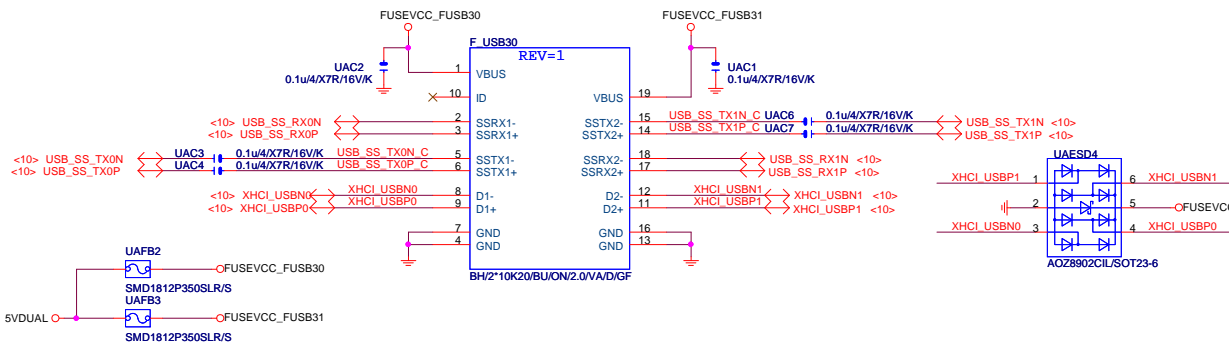
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SizeCustomDocument NumberGA-F2A85XM-HD3Rev1.01

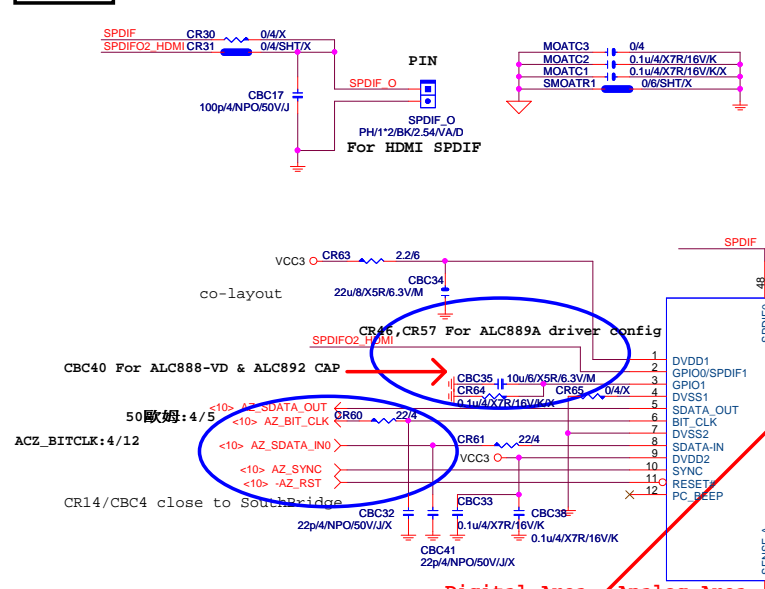
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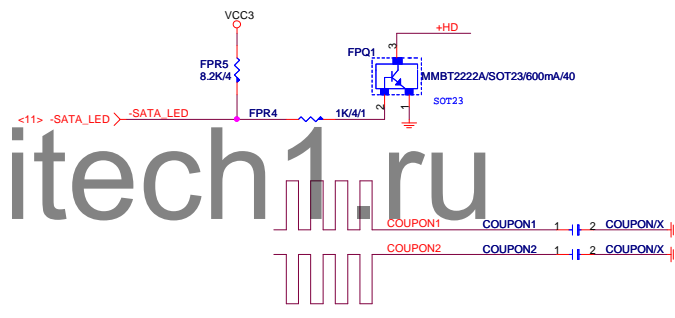
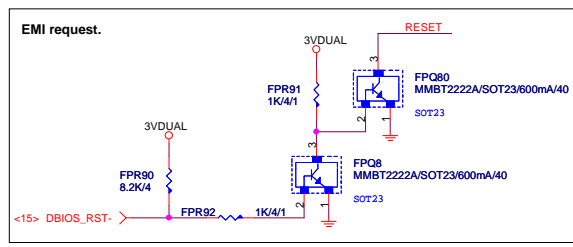
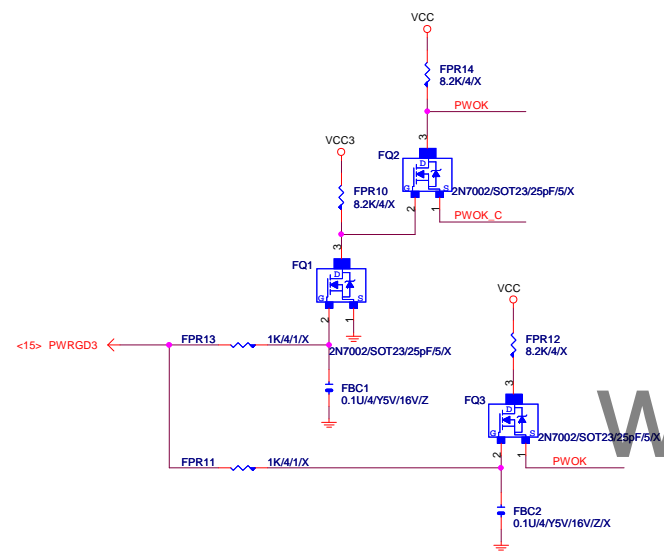
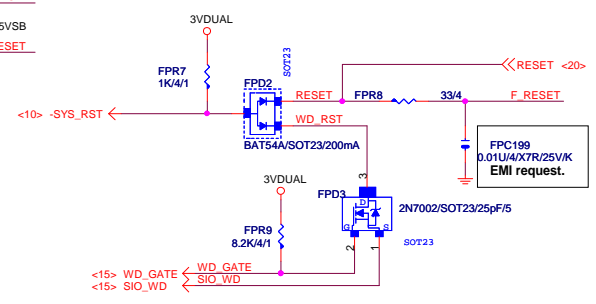
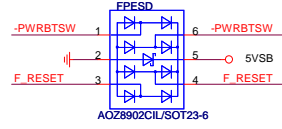
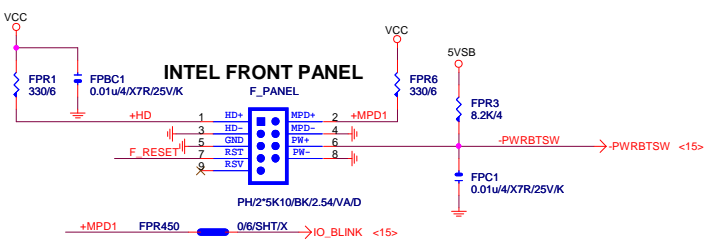






SPDIF_OUT





ATX POWER CONNECTOR

